TST660

PCI Development Platform

400-0047-001
5/22/01

Description

TST660 is a development unit supporting two 64-bit PCI slots. This unit can generate several different clock frequencies and several different PCI test patterns. It can also allow the TA700 to become a PCI agent so the TA700 can perform data transfer with another PCI agent on the bus.

TST660 has an on board arbiter for handling REQ#t and GNT# but not for interrupt handling.

TST660 is most useful during initial phase of a board/device bring up making sure the system under test is not subject to system crashes until the card can handle the basic PCI protocols.

Configuration registers of new designs can be tested using TA700 and TST660 without any software requirement.

Device compliance checks may also be performed using just TA700 and TST660 cards.

TST660-3 is assembled with 3V 64-bit connectors
TST660-5 is assembled with 5V 64-bit connectors

SETUP

S1 Dip Switches

S1-1	S1-2	S1-3	Clock Frequency
On	On	On	66 MHz
Off	Off	On	50 MHz
On	Off	On	User Oscillator/100 Mhz removable Oscillator
On	On	Off	33 MHz
Off	Off	Off	25 MHz
On	Off	Off	½ User

S1-4
On   Bus is Tri-stated for TA660 to read/write to BUT

S1-4
Off   Pattern Generation by TST660

S1-5
Off   Read Transaction Generated
On   Write Transaction Generated

S1-6
Off   Generate Data transfer with Disconnect
On   Generate normal Data Transfers

S1-7
Off   Burst of 256 Data Transfers
On   Burst of Two data Transfers

S2 Push Button

S2 issues Reset to the pattern generator as well as the PCI bus.

Jumpers

1- JP1
   Installed on the left = sets 66MENB to the BUT
   Installed on the right = set 66MENB to GND

2- JP2
   Factory use

3- JP3
   –12 Volt input. Square pad = GND, Round pad = -12V.

4- JP4
   VIO to the PCI slots as marked

5- JP5 (If you are not using an ATX Power Supply, This jumper must be installed)
   Installed, on-board regulator generates 3.3V for the PCI slots
   Uninstalled, ATX power supply generates 3.3V for the PCI slots